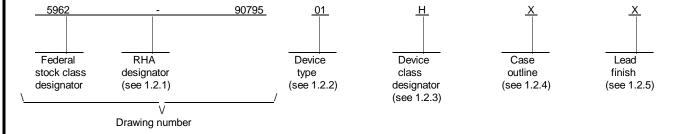
								R	EVISI	ONS			1							
LTR					D	ESCF	RIPTIC	N					DATE (YR-MO-DA)			APPROVED		D_		
Α	Ma	ke cha	inges	to tab	le I.	Rewrit	te enti	re doc	umer	nt.				94-0)1-18		K. A. Cottongim			
			THE	ORIG	SINAL	. FIRS	T PAC	GE OF	THIS	DRA	WING	S HAS	BEEN	N REP	PLACE	ĒD				
REV	Τ		THE	ORIG	SINAL	. FIRS	T PAC	GE OF	THIS	DRA	WING	S HAS	BEEN	N REP	PLACE	ĒD		Ι	<u> </u>	<u> </u>
			THE	ORIG	SINAL	. FIRS	T PAC	SE OF	THIS	DRA	WING	6 HAS	BEEN	N REF	PLACE	ĒD				
SHEET			THE	ORIG	SINAL	. FIRS	T PAC	GE OF	THIS	DRA	WING	6 HAS	BEEN	N REP	PLACE	ED				
SHEET			THE	ORIG	SINAL	. FIRS	T PAC	GE OF	THIS	DRA	WING	6 HAS	BEEN	N REF	PLACE	ED .				
SHEET REV SHEET REV STAT			THE	ORIG		. FIRS	T PAC	SE OF	THIS	DRA	WING	6 HAS	BEEN	N REF	PLACE	ED A	A	A		
SHEET REV SHEET REV STAT			THE	RE		. FIRS											A 11	A 12		
REV SHEET REV SHEET REV STAT OF SHEET			THE	RE SH	V) BY	A	A	A	A 4	A 5	A 6	A 7	A 8	A 9	A 10	11 JPPL	12 -Y CI	ENT	EF
SHEET REV SHEET REV STAT DF SHEET PMIC N/A STANI	DARI	RY		RE SH PREI Gar	V EET	D BY	A	A	A	A 4	A 5	A 6	A 7	A 8	A 9	A 10	11 JPPL	12 -Y CI	ENT	EF
SHEET REV SHEET REV STAT OF SHEET MIC N/A STANI MIL DR THIS DRAW FOR U	DARI LITAF AWIN ING IS JSE BY	RY NG AVAILA ' ALL))	RE SH PREI Gar CHE Rote	V EET PARED TO Y Zahn CKED I Deert M.) BY BY Heber	A 1	A	A	A 4 D	A 5 EFEI	A 6 NSE	A 7 ELEGO	A 8 CTRO	A 9 ONIC	A 10 CS SU	JPPL 4544 BIT, F	Y CI		EF
SHEET REV SHEET REV STAT OF SHEET MIC N/A STANI MIL DR THIS DRAW FOR U DEP/AND AGE	DARI LITAF AWIN ING IS JSE BY ARTME ENCIES	RY NG AVAILA ALL NTS OF TH) ABLE	RE SH PREI Gar CHE Rob	V EET PARED Ty Zahn CKED I pert M.	BY Heber D BY Heckma	A 1	A 2	A	A 4 D MIC SP	A 5 EFEI	A 6 NSE CIRC	A 7 ELEG DAY	A 8 CTRO TON	A 9 ONIC	A 10 16-E R, HY	JPPL 4544 BIT, I	12 Y CI 4 HIGH	l	EF
SHEET REV SHEET REV STAT OF SHEET MIL MIL DR THIS DRAW FOR L DEPA AND AGE DEPARTME	DARI LITAF AWIN ING IS JSE BY ARTME ENCIES	RY NG AVAILA Y ALL NTS OF TH DEFEN) ABLE	RE SH PREI Gar CHE Rob	V EET PARED Ty Zahn CKED I pert M.	BY Heber D BY Heckma APPRO 90-1	A 1	A 2	A	A 4 D	A 5 EFEI	A 6 NSE CIRC, A/D	A 7 ELEGO	A 8 CTRO TON LINE	A 9 ONIC	A 10 16-E R, HY	JPPL 4544 BIT, I	Y CI	l	EF

1. SCOPE

- 1.1 <u>Scope</u>. This drawing forms a part of a one part one part number documentation system (see 6.6 herein). This drawing describes device requirements for hybrid microcircuits to be processed in accordance with MIL-H-38534. Two product assurance classes, military high reliability (device class H) and space application (device class K) and a choice of case outlines and lead finishes are available and are reflected in the Part or Identifying Number (PIN). When available, a choice of radiation hardness assurance levels are reflected in the PIN.
 - 1.2 PIN. The PIN shall be as shown in the following example:



- 1.2.1 Radiation hardness assurance (RHA) designator. Device classes H and K RHA marked devices shall meet the MIL-H-38534 specified RHA levels and shall be marked with the appropriate RHA designator. A dash (-) indicates a non-RHA device.
 - 1.2.2 <u>Device type(s)</u>. The device type(s) shall identify the circuit function as follows:

Device type	Generic number	Circuit function	Maximun linearity error	
01	HS9576S	16-bit A/D converter	±0.006% FSR	
02	HS9576T	16-bit A/D converter	±0.003% FSR	

1.2.3 Device class designator. This device class designator shall be a single letter identifying the product assurance level as follows:

Device class

Device requirements documentation

H or K

Certification and qualification to MIL-H-38534

1.2.4 Case outline(s). The case outline(s) shall be as designated in MIL-STD-1835 and as follows:

Outline letter	Descriptive designator	<u>Terminals</u>	Package style
Χ	See figure 1	32	Dual-in-line

1.2.5 <u>Lead finish</u>. The lead finish shall be as specified in MIL-H-38534 for classes H and K. Finish letter "X" shall not be marked on the microcircuit or its packaging. The "X" designation is for use in specifications when lead finishes A, B, and C are considered acceptable and interchangeable without preference.

STANDARDIZED
MILITARY DRAWING
DEFENSE ELECTRONICS SUPPLY CENTER
DAYTON, OHIO 45444

SIZE A		5962-90795
	REVISION LEVEL A	SHEET 2

1.3 Absolute maximum ratings. 1/

 $\begin{array}{lll} \text{Positive supply voltage range } (\text{V}_{\text{CC}}) \\ \text{Negative supply voltage range } (\text{V}_{\text{EE}}) \\ \text{Logic supply voltage range } (\text{V}_{\text{DD}}) \\ \text{Analog ground to logic ground} \end{array}$ 0 V dc to +16 V dc 0 V dc to -16 V dc -0.5 V dc to +7.0 V dc

0.5 V dc

Logic inputs to logic ground 0 V dc to +6.5 V dc Analog inputs to analog ground 0 V dc to +16.5 V dc Voltage on logic outputs 0 V dc to 7.0 V dc 20W

40° C/W +300° C -65° C to +150° C

+170° C

1.4 Recommended operating conditions.

+14.55 V dc to +15.45 V dc -14.55 V dc to -15.45 V dc Logic supply voltage range (V_{DD}) +4.75 V dc to +5.25 V dc -55° C to +125° C

2. APPLICABLE DOCUMENTS

2.1 Government specification, standards, and handbook. Unless otherwise specified, the following specification, standards, and handbook of the issue listed in that issue of the Department of Defense Index of Specifications and Standards specified in the solicitation, form a part of this drawing to the extent specified herein.

SPECIFICATION

MILITARY

MIL-H-38534 - Hybrid Microcircuits, General Specification for.

STANDARDS

MILITARY

MIL-STD-883 - Test Methods and Procedures for Microelectronics.

MIL-STD-973 - Configuration Management. MIL-STD-1835 - Microcircuit Case Outlines.

HANDBOOK

MILITARY

MIL-HDBK-780 - Standardized Military Drawings.

(Copies of the specification, standards, and handbook required by manufacturers in connection with specific acquisition functions should be obtained from the contracting activity or as directed by the contracting activity.)

2.2 Order of precedence. In the event of a conflict between the text of this drawing and the references cited herein, the text of this drawing shall take precedence.

1/ Stresses above the absolute maximum rating may cause permanent damage to the device. Extended operation at the maximum levels may degrade performance and affect reliability.

5962-90795

3

SHEET

STANDARDIZED MILITARY DRAWING	SIZE A	
DEFENSE ELECTRONICS SUPPLY CENTER DAYTON, OHIO 45444		REVISION LEVEL A

- 3. REQUIREMENTS
- 3.1 Item requirements. The individual item requirements shall be in accordance with MIL-H-38534 and as specified herein.
- 3.2 <u>Design, construction, and physical dimensions</u>. The design, construction, and physical dimensions shall be as specified in MIL-H-38534 and herein.
 - 3.2.1 Case outline(s). The case outline(s) shall be in accordance with 1.2.4 herein and figure 1.
 - 3.2.2 Terminal connections. The terminal connections shall be as specified on figure 2.
- 3.3 <u>Electrical performance characteristics</u>. Unless otherwise specified herein, the electrical performance characteristics are as specified in table I and shall apply over the full specified operating temperature range.
- 3.4 <u>Electrical test requirements</u>. The electrical test requirements shall be the subgroups specified in table II. The electrical tests for each subgroup are described in table I.
- 3.5 Marking. Marking shall be in accordance with MIL-H-38534. The part shall be marked with the PIN listed in 1.2 herein. In addition, the manufacturer's PIN may also be marked as listed in QML-38534.
- 3.6 <u>Manufacturer eligibility</u>. In addition to the general requirements of MIL-H-38534, the manufacturer of the part described herein shall maintain the electrical test data (variables format) from the initial quality conformance inspection group A lot sample, produced on the certified line, for each device type listed herein. The data should also include a summary of all parameters manually tested, and for those which, if any, are guaranteed. This data shall be maintained under document revision level control by the manufacturer and be made available to the preparing activity (DESC-EC) upon request.
- 3.7 <u>Certificate of compliance</u>. A certificate of compliance shall be required from a manufacturer in order to supply to this drawing. The certificate of compliance submitted to DESC-EC shall affirm that the manufacturer's product meets the requirements of MIL-H-38534 and the requirements herein.
- 3.8 <u>Certificate of conformance</u>. A certificate of conformance as required in MIL-H-38534 shall be provided with each lot of microcircuits delivered to this drawing.
 - 4. QUALITY ASSURANCE PROVISIONS
 - 4.1 Sampling and inspection. Sampling and inspection procedures shall be in accordance with MIL-H-38534.
 - 4.2 Screening. Screening shall be in accordance with MIL-H-38534. The following additional criteria shall apply:
 - a. Burn-in test, method 1015 of MIL-STD-883.
 - (1) Test condition A, B, C, or D. The test circuit shall be maintained by the manufacturer under document revision level control and shall be made available to either DESC-EC or the acquiring activity upon request. Also, the test circuit shall specify the inputs, outputs, biases, and power dissipation, as applicable, in accordance with the intent specified in test method 1015 of MIL-STD-883.
 - (2) T_A as specified in accordance with table I of method 1015 of MIL-STD-883.
 - b. Interim and final electrical test parameters shall be as specified in table II herein, except interim electrical parameter tests prior to burn-in are optional at the discretion of the manufacturer.

STANDARDIZED
MILITARY DRAWING
DEFENSE ELECTRONICS SUPPLY CENTER
DAYTON, OHIO 45444

SIZE A		5962-90795
	REVISION LEVEL A	SHEET 4

Test	Symbol	TABLE I. <u>Electrical perfo</u> Conditions <u>1</u> /	Group A	Device	Limits		Unit
	Syss.	-55° C ≤ T _C ≤ +125° C unless otherwise specified	subgroups	type	Min	Max	-
Input voltage	V _{IN}	Bipolar	1,2,3	01,02	-2.5 -5.0 -10.0	+2.5 +5.0 +10.0	V
		Unipolar	1,2,3	01,02	0 0 0	+5.0 +10.0 +20.0	V
High level input voltage	V _{IH}		1,2,3	01,02	2.0		V
Low level input voltage	V _{IL}		1,2,3	01,02		0.8	V
High level output voltage	V _{OH}		1,2,3	01,02	2.4		V
Low level output voltage	V _{OL}		1,2,3	01,02		0.40	V
Supply currents	I _{cc}	V _{CC} = +15.45 V dc	1,2,3	01,02		+23	mA
	I _{EE}	V _{EE} = -15.45 V dc	1,2,3	01,02		-38	mA
	I _{DD}	V _{DD} = +5.25 V dc	1,2,3	01,02		+37	mA
Integral linearity error	ILE	End-point definition, T _C = +25° C	4	01	-0.006	+0.006	%FSR
				02	-0.003	+0.003	
		T _C = +125° C	5	01	-0.036	+0.036	%FSR
				02	-0.023	+0.023	%FSR
		T _C = -55° C	6	01	-0.030	+0.030	%FSR
				02	-0.019	+0.019	%FSR

See footnotes at end of table.

STANDARDIZED MILITARY DRAWING	SIZE A		5962-90795
DEFENSE ELECTRONICS SUPPLY CENTER DAYTON, OHIO 45444		REVISION LEVEL A	SHEET 5

		TABLE I. Electrical performance	e characteristics -	Continued.			
Test	Symbol	Conditions <u>1</u> / -55° C ≤ T _C ≤ +125° C	Group A subgroups	Device type	Limits		Unit
		unless otherwise specified			Min	Max	
Differential linearity error	DLE	T _C = +25° C	4	01,02	-0.006	+0.006	%FSR
		T _C = +125° C	5	01	-0.006	+0.046	%FSR
				02	-0.006	+0.026	%FSR
		T _C = -55° C	6	01	-0.006	+0.038	%FSR
				02	-0.006	+0.022	%FSR
Gain error	A _E	Adjustable to zero, T _C = +25° C	4	01,02	-0.2	+0.2	%FSR
		T _C = +125° C	5	01,02	-0.35	+0.35	%FSR
		T _C = -55° C	6	01,02	-0.32	+0.32	%FSR
Offset error	O _E	Unipolar (adjustable to zero), T _C = +25° C	4	01,02	-0.1	+0.1	%FSR
		Unipolar (adjustable to zero), T _C = -55° C, +125° C	5,6	01,02	-0.125	+0.125	%FSR
		Bipolar (adjustable to zero), T _C = +25° C	4	01,02	-0.2	+0.2	%FSR
		Bipolar (adjustable to zero), T _C = -55° C, +125° C	5,6	01,02	-0.30	+0.30	%FSR
Noise at transitions (3 sigma) 2/		Peak to peak	4,5,6	01,02		+0.003	%FSR

See footnotes at end of table.

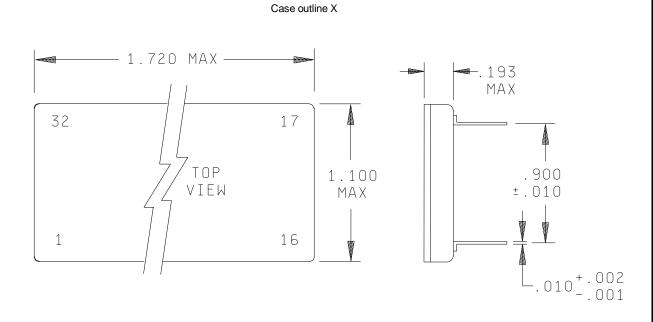
STANDARDIZED MILITARY DRAWING	SIZE A		5962-90795
DEFENSE ELECTRONICS SUPPLY CENTER DAYTON, OHIO 45444		REVISION LEVEL A	SHEET 6

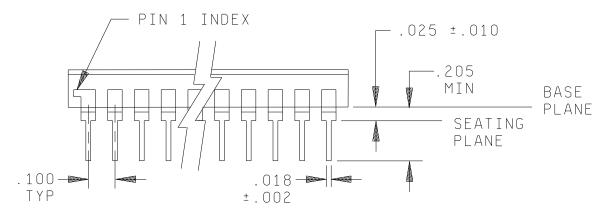
TABLE I. <u>Electrical performance characteristics</u> - Continued.							
Test	Test Symbol Conditions $\underline{1}/$ Group A Device type	Device type		_imits	Unit		
		unless otherwise specified			Min	Max	
12-bit conversion time	t _{con}	Pin 32 to pin 13, 12-bit accurate	9,10,11	01,02		13	μs
14-bit conversion time	t _{con}	Pin 32 to pin 15, 14-bit accurate	9,10,11	01,02		15	μs
16-bit conversion time	t _{con}	Pin 32 open, 16-bit accurate	9,10,11	01,02		17	μs

 $[\]underline{1}/\text{ Unless otherwise specified, V}_{CC} = +14.55 \text{ V dc to } +15.45 \text{ V dc, V}_{EE} = -14.55 \text{ V dc to } -15.45 \text{ V dc, and V}_{DD} = +4.75 \text{ V dc to } +5.25 \text{ V dc.}$

STANDARDIZED MILITARY DRAWING	SIZE A		5962-90795
DEFENSE ELECTRONICS SUPPLY CENTER DAYTON, OHIO 45444		REVISION LEVEL A	SHEET 7

^{2/} Parameter shall be tested as part of device initial chacterization and after design and process changes. Parameter shall be guaranteed to the limits specified in table I for all lots not specifically tested.





Inches	mm	Inches	mm
1.720	43.69	.025	0.64
1.100	27.94	.018	0.46
.900	22.86	.010	0.25
.205	5.21	.002	0.05
.193	4.90	.001	0.03
.100	2.54		

NOTES:

- Dimensions are in inches.
- 2.
- Metric equivalents are given for general information only. Unless otherwise specified, tolerance is ±.005 (0.13 mm).
- Pin numbers are for reference only.

FIGURE 1. Case outline(s).

STANDARDIZED MILITARY DRAWING	SIZE A		5962-90795
DEFENSE ELECTRONICS SUPPLY CENTER DAYTON, OHIO 45444		REVISION LEVEL A	SHEET 8

Device types	01, 02	Device types	01, 02
Case outline	Х	Case outline	Х
Terminal number	Terminal symbol	Terminal number	Terminal symbol
1	Bit 1 (MSB)	17	Serial out
2	Bit 2	18	Status
3	Bit 3	19	Digital ground
4	Bit 4	20	Clock out
5	Bit 5	21	-15 V (V _{EE})
6	Bit 6	22	Analog ground
7	Bit 7	23	Clock rate control
8	Bit 8	24	20 V input
9	Bit 9	25	10 V input
10	Bit 10	26	Bipolar offset
11	Bit 11	27	Comparator input
12	Bit 12	28	+15 V (V _{CC})
13	Bit 13 (LSB for 13 Bits)	29	Gain adjust
14	Bit 14 (LSB for 14 Bits)	30	+5 V (V _{DD})
15	Bit 15	31	Convert command
16	Bit 16	32	Short cycle

FIGURE 2. Terminal connections.

STANDARDIZED MILITARY DRAWING	SIZE A		5962-90795
DEFENSE ELECTRONICS SUPPLY CENTER		REVISION LEVEL	SHEET
DAYTON, OHIO 45444		A	9

TABLE II. Electrical test requirements.

MIL-H-38534 test requirements	Subgroups (in accordance with MIL-H-38534, group A test table)
Interim electrical parameters	
Final electrical test parameters	1*,2,3,4,5,6,9,10,11
Group A test requirements	1,2,3,4,5,6,9,10,11
Group C end-point electrical parameters	1,2,3,4
MIL-STD-883, group E end-point electrical parameters for RHA devices	Subgroups ** (in accordance with method 5005, group A test table)

- * PDA applies to subgroup 1.
- ** When applicable to this standardized military drawing, the subgroups shall be defined.
- 4.3 Quality conformance inspection. Quality conformance inspection shall be in accordance with MIL-H-38534 and as specified herein.
- 4.3.1 Group A inspection. Group A inspection shall be in accordance with MIL-H-38534 and as follows:
 - a. Tests shall be as specified in table II herein.
 - b. Subgroups 7 and 8 shall be omitted.
- 4.3.2 Group B inspection. Group B inspection shall be in accordance with MIL-H-38534.
- 4.3.3 Group C inspection. Group C inspection shall be in accordance with MIL-H-38534 and as follows:
 - a. End-point electrical parameters shall be as specified in table II herein.
 - b. Steady-state life test, method 1005 of MIL-STD-883.
 - (1) Test condition A, B, C, or D. The test circuit shall be maintained by the manufacturer under document revision level control and shall be made available to either DESC-EC or the acquiring activity upon request. Also, the test circuit shall specify the inputs, outputs, biases, and power dissipation, as applicable, in accordance with the intent specified in test method 1005 of MIL-STD-883.
 - (2) T_A as specified in accordance with table I of method 1005 of MIL-STD-883.
 - (3) Test duration: 1,000 hours, except as permitted by method 1005 of MIL-STD-883.

STANDARDIZED MILITARY DRAWING DEFENSE ELECTRONICS SUPPLY CENTER DAYTON, OHIO 45444	SIZE A		5962-90795
		REVISION LEVEL A	SHEET 10

- 4.3.4 Group D inspection. Group D inspection shall be in accordance with MIL-H-38534.
- 4.3.5 <u>Group E inspection</u>. Group E inspection is required only for parts intended to be marked as radiation hardness assured (see 3.5 herein). RHA levels for device classes H and K shall be M, D, R, and H. RHA quality conformance inspection sample tests shall be performed at the RHA level specified in the acquisition document.
 - a. RHA tests for device classes H and K for levels M, D, R, and H shall be performed through each level to determine at what levels the devices meet the RHA requirements. These RHA tests shall be performed for initial qualification and after design or process changes which may affect the RHA performance of the device.
 - b. End-point electrical parameters shall be as specified in table II herein.
 - c. Prior to total dose irradiation, each selected sample shall be assembled in its qualified package. It shall pass the specified group A electrical parameters in table I for subgroups specified in table II herein.
 - d. For device classes H and K, the devices shall be subjected to radiation hardness assured tests as specified in MIL-H-38534 for RHA level being tested, and meet the postirradiation end-point electrical parameter limits as defined in table I at $T_{\Delta} = +25^{\circ}$ C ± 5 percent, after exposure.
 - e. Prior to and during total dose irradiation testing, the devices shall be biased to establish a worst case condition as specified in the radiation exposure circuit.
 - For device classes H and K, subgroups 1 and 2 in table V, method 5005 of MIL-STD-883 shall be tested as appropriate for device construction.
 - g. When specified in the purchase order or contract, a copy of the RHA delta limits shall be supplied.
 - 5. PACKAGING
 - 5.1 Packaging requirements. The requirements for packaging shall be in accordance with MIL-H-38534.
 - 6. NOTES
- 6.1 <u>Intended use</u>. Microcircuits conforming to this drawing are intended for use for Government microcircuit applications (original equipment), design applications, and logistics purposes.
- 6.2 <u>Replaceability</u>. Microcircuits covered by this drawing will replace the same generic device covered by a contractor-prepared specification or drawing.
- 6.3 <u>Configuration control of SMD's</u>. All proposed changes to existing SMD's will be coordinated with the users of record for the individual documents. This coordination will be accomplished in accordance with MIL-STD-973 using DD Form 1692, Engineering Change Proposal.
- 6.4 <u>Record of users</u>. Military and industrial users shall inform Defense Electronics Supply Center when a system application requires configuration control and the applicable SMD. DESC will maintain a record of users and this list will be used for coordination and distribution of changes to the drawings. Users of drawings covering microelectronic devices (FSC 5962) should contact DESC-EC, telephone (513) 296-6047.
 - 6.5 Comments. Comments on this drawing should be directed to DESC-EC, Dayton, Ohio 45444, or telephone (513) 296-5373.

STANDARDIZED MILITARY DRAWING	SIZE A		5962-90795
DEFENSE ELECTRONICS SUPPLY CENTER		REVISION LEVEL	SHEET
DAYTON, OHIO 45444		A	11

6.6 One part - one part number system. The one part - one part number system described below has been developed to allow for transitions between identical generic devices covered by the three major microcircuit requirements documents (MIL-H-38534, MIL-I-38535, and 1.2.1 of MIL-STD-883) without the necessity for the generation of unique PIN's. The three military requirements documents represent different class levels, and previously when a device manufacturer upgraded military product from one class level to another, the benefits of the upgraded product were unavailable to the Original Equipment Manufacturer (OEM), that was contractually locked into the original unique PIN. By establishing a one part number system covering all three documents, the OEM can acquire to the highest class level available for a given generic device to meet system needs without modifying the original contract parts selection criteria.

Military documentation format	Example PIN under new system	Manufacturing source listing	Document listing
New MIL-H-38534 Standardized Military Drawings	5962-XXXXXZZ(H or K)YY	QML-38534	MIL-BUL-103
New MIL-I-38535 Standardized Military Drawings	5962-XXXXXZZ(Q or V)YY	QML-38535	MIL-BUL-103
New 1.2.1 of MIL-STD-883 Standardized Military Drawings	5962-XXXXXZZ(M)YY	MIL-BUL-103	MIL-BUL-103

6.7 <u>Sources of supply for device classes H and K</u>. Sources of supply for device classes H and K are listed in QML-38534. The vendors listed in QML-38534 have submitted a certificate of compliance (see 3.7 herein) to DESC-EC and have agreed to this drawing.

STANDARDIZED
MILITARY DRAWING
DEFENSE ELECTRONICS SUPPLY CENTER
DAYTON, OHIO 45444

SIZE A		5962-90795
	REVISION LEVEL A	SHEET 12

STANDARDIZED MILITARY DRAWING SOURCE APPROVAL BULLETIN

DATE: 94-01-18

Approved sources of supply for SMD 5962-90795 are listed below for immediate acquisition only and shall be added to QML-38534 during the next revision. QML-38534 will be revised to include the addition or deletion of sources. The vendors listed below have agreed to this drawing and a certificate of compliance has been submitted to and accepted by DESC-EC. This bulletin is superseded by the next dated revision of QML-38534.

Standardized military drawing PIN	Vendor CAGE number	Vendor similar PIN <u>1</u> /
5962-9079501HXX	33256	HS9576S/B
5962-9079502HXX	33256	HS9576T/B

1/ Caution. Do not use this number for item acquisition. Items acquired to this number may not satisfy the performance requirements of this drawing.

Vendor CAGE Vendor name number and address

33256 Sipex Corporation 22 Linnell Circle

Billerica, MA 01821-3985

The information contained herein is disseminated for convenience only and the Government assumes no liability whatsoever for any inaccuracies in this information bulletin.